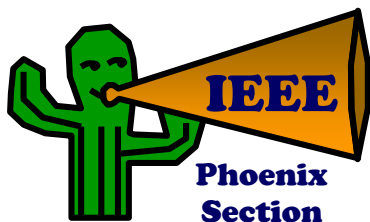


The Valley Megaphone

Newsletter of the
**Institute of Electrical and
Electronics Engineers, Inc.,
Phoenix Section**
January, 2016
Volume XXX, Number 1



Executive Committee 2016

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TBD

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that page)

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IEEE Phoenix Section on-line updates can be found at
<http://sites.ieee.org/phoenix/> and on LinkedIn
at:<http://www.linkedin.com/groups?qid=2765918>
and on Facebook at:
<https://www.facebook.com/IEEEPhoenixSection>

Please send announcements for the *Valley
Megaphone* to Wei Xu at Wei.Dr.Xu@ieee.org for
inclusion in the Section Calendar.

**All meetings announced in the
Phoenix Section Megaphone or on
the Phoenix Section Calendar are
open to everyone (IEEE members
and non-Members)**

Chapters

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Computer Society

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CPMT Society

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EMBS Chapter

TBD

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Solid State Circuits

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Teacher-In-Service

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Waves & Devices Society

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Life Members

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Women In Engineering

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Young Professionals

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U – News (for Student Members)

Updates of Student Advisors and Committee Members

Each Student Branch noted on the right side of this page should review current information on Advisors and Student Committee Members and forward to my attention within this week, as we are reviewing contacts for reporting and activities including Student Monthly Meetings.

S. Diane Smith
602-749-4601
sdianesmith@computer.org
Student Activities Chair

Update from DeVry Phoenix-Engineering Student Branch

The IEEE Student Branch and Computer Society at DeVry University, Phoenix are co-sponsoring and co-managing with the University a Science Technology Engineering and Math (S.T.E.M.) exhibit at the 2015 Arizona State Fair. We have a 24 x 48 foot space that will house various high school and collegiate STEM projects often representing Capstone activities. In addition, four STEM workshops will be conducted by the students: Cyber Security (“Can You Hack It?”); personal computer repair (“DIY Computer Fixes”); beta-test student designed simulations (“Would you like to play a game?”); and breadboard kit building (“Make your own Night Light” and “Hidden Buzzer”). The static displays will be visible the duration of the Fair. The workshops are scheduled and published and will occur on the four weekends of the Fair. Section members and their families and friends are invited to stop by the exhibit and join us in the fun at the Arizona State Fair!

Roger S. Gulledge
Counselor, IEEE Student Branch
DeVry University
rgulledge@devry.edu

Student Branches

ASU Main, Engineering

Chair: Ngoni Mugwisi
480-567-4299,
ieeeeasuchair@gmail.com
Advisor: Cihan Tepedelenlioglu,
480-965-6623, cihan@asu.edu

ASU Main, Computer Society

Chair: TBD
Advisor: Guoliang Xue
480-965-6218, xue@asu.edu

ASU Main, Power and Energy Society

Chair: Nikita Singhal (nsinghal@asu.edu)
Co-Chair: Deepak Ramasubramanian
(dramasu1@asu.edu)
Advisor: Kory Hedman
kory.hedman@asu.edu

ASU Polytechnic

Chair: Josh Carroll
jkcarrol@asu.edu
Elizabeth Long
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Advisor: : Dr. John M. Parsey, Jr.,
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DeVry, Phoenix – Engineering

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Advisor: Deanna Davis
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DeVry, Phoenix – Computer Society

Chair: TBD
Past Chair: Zak Burgess
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Advisor: Deanna Davis
602-749-4500
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NAU, Engineering

Chair: Jeremy Johnson
Advisor: Niranjan Venkatraman
v.niranjan@ieee.org

Embry-Riddle, Prescott

Chair: Lisa M. Ferguson
FERGUSL2@my.erau.edu
Advisor: John E. Post
postj@erau.edu

U – Newsbytes

ADVANCED MANUFACTURING SYSTEMS (11362)

The Ira A. Fulton Schools of Engineering at Arizona State University (ASU) is hiring faculty to support a broad initiative in advanced manufacturing. We are particularly interested in expanding our capabilities in the research areas that lie between the areas of basic research in the physics/materials/chemistry aspects of manufacturing processes and applied research at the production system and supply chain levels.

Specific areas of interest include, but are not limited to: multi-material manufacturing systems; hybrid additive-subtractive manufacturing process integration; scalable manufacturing at the limits of size, temperatures and material properties; product design strategies for additive manufacturing systems; non-destructive testing-manufacturing integration; automation strategies and technologies for hybrid manufacturing; modeling of advanced manufacturing systems and processes.

We seek applicants who will contribute to our academic programs, promote transdisciplinary teaching and research, and help the University to achieve its aspirations, including enabling student success, transforming society, valuing entrepreneurship, and conducting use-inspired research.

Faculty members are expected to develop an internationally recognized and externally funded research program, adopt effective pedagogical practices in the development and delivery of graduate and undergraduate courses, advise both undergraduate and graduate student research and projects, and undertake service activities.

Required qualifications:

Required qualifications include an earned doctorate in Manufacturing Engineering, Materials Science and Engineering, Mechanical Engineering, Industrial Engineering, or a related field, along with demonstrated evidence of research and teaching excellence as appropriate to the candidate's rank.

Desired qualifications:

Desired qualifications include a demonstrated commitment to a collaborative approach to research and the use of modern pedagogical practices in teaching.

Appointment will be at the **Assistant, Associate, or Full Professor** rank commensurate with the candidate's experience and accomplishments, beginning August 2016.

While the faculty appointment may be in any of the six Fulton Schools of Engineering, The Polytechnic School, located at ASU's Polytechnic campus in Mesa, Arizona, is currently the most involved in the search. The Polytechnic School offers related degrees at the bachelor's level in Engineering and Manufacturing Engineering, at the master's level in Engineering, and at the Ph.D. level in Systems Engineering. Additionally, the Polytechnic School houses the premier additive manufacturing and research center in the Southwest, providing strong support for the advancing ASU's research and development in Advanced Manufacturing. The facility features over \$2M in state-of-the-art polymer, metal, and composite materials 3D printing equipment as well as advanced materials processing and analysis capabilities.

How to apply:

To apply, please submit a single PDF file to advanced.manufacturing.faculty@asu.edu that includes:

- Cover letter.
- Current CV.
- Statement describing research interests (two pages maximum).
- Statement describing teaching interests and philosophy (two pages maximum).
- Contact information for three references.

Review of applications will begin November 16, 2015; if not filled, reviews will occur on the 1st and 15th of the month thereafter until the search is closed.

For more information or questions about these positions, please contact the search committee chair, Jennifer Bekki (jennifer.bekki@asu.edu).

Arizona State University is a VEVRAA Federal Contractor and an Equal Opportunity/Affirmative Action Employer. All qualified applicants will be considered without regard to race, color, sex, religion, national origin, disability, protected veteran status, or any other basis protected by law. See ASU's full [non-discrimination statement](#) (ACD 401) at [_](#) and the [Title IX statement](#).

ASU offers applicants an opportunity to voluntarily disclose information for the University's affirmative action plan; applicants may complete an [EEO survey](#) for the position they are applying for online.

Information you'll need to complete the survey:

Job Number: 11362

Job Title: POLY Advanced Manufacturing

Department Name: Engineering

BOEING ENGINEERING JOB FAIR

TECHEXPO is working on behalf of The Boeing Company to recruit engineers to their Oklahoma City location.

At Boeing, engineers share a passion to redefine what's possible. To turn dreams into reality. To bring world-class innovation to market. Join the Boeing team and you can design the next generation of amazing products.

Whether your engineering background is electrical, software, systems or mechanical/structural, The Boeing Company has opportunities for you in Oklahoma City, Oklahoma.

[Click here](#) to review all the job openings, apply and bring your ideas and skills to Boeing.

Boeing representatives can also review your resume in person and discuss career options while they are in Mesa.

Thursday, January 21 • 10:00 a.m. – 2:00 p.m.

Hilton Phoenix / Mesa

Room: Pueblo Room

1011 W. Holmes Ave, Mesa, AZ 85210

[RSVP](#) for this event.

(Your RSVP is preferred but not required.)

Please share this invitation with your friends and colleagues who may also be interested in career opportunities with Boeing.

Current job openings include:

- Avionics Engineer
- Electrical & Electronics Engineer
- Electromagnetic Effects Engineer
- Radar Analysis Engineer
- Software Engineer
- Structural Analysis Engineer
- Systems Security Engineer
- Technical Designer

At Boeing, opportunities are created every day. Don't miss out. If you're unable to attend this event in Mesa, sign up for their [Talent Network](#) and a Boeing representative will contact eligible applicants to find out more about your career interests.

If you're currently an officer or employee of the U.S. Government, please remember that once you start "seeking employment" with Boeing, you must disqualify yourself from taking action on any official matters involving Boeing. To help avoid any inadvertent violation of this requirement, Boeing cannot contact you to discuss the position further until you have created a profile and answered the questions regarding your current government employment.

<https://www.facebook.com/BoeingCareers>

<https://twitter.com/BoeingCareers>

<https://www.linkedin.com/company/1384>

<https://www.youtube.com/user/Boeing>

Boeing is an Equal Opportunity Employer. Employment decisions are made without regard to race, color, religion, sex, sexual orientation, gender identity, national origin, disability, veteran status or other characteristics protected by law.

Update from IEEE Power and Energy Society (PES) ASU Student Chapter

Our student chapter was formalized and approved on 10th February 2015 with a geo code of SBC00101A. The members of the executive committee of the student chapter were decided by a vote in the inaugural meeting in March 2015. The executive committee is as follows:

Chairpersons:

Nikita Singhal (nsinghal@asu.edu)
Deepak Ramasubramanian (dramasu1@asu.edu)

Vice-chairpersons:

Shruti Rao (srao12@asu.edu)
Sohom Datta (sdatta9@asu.edu)

Secretaries:

Pranavamorthy Balasubramanian (pbalasu3@asu.edu)
Mojgan Mehdiabadi (mojgan.hedayatimehdiabad@asu.edu)

Treasurers:

Jonghwan Kwon (Jonghwan.Kwon@asu.edu)
Samet Arda (sarda1@asu.edu)

Social chairpersons:

Mojdeh Khorsand (mojdeh.khorsand@asu.edu)
Chao Li (Chao.Li.cidse@asu.edu)

Networking coordinator:

Yousef Al-Abdullah (yalabdul@asu.edu)

Faculty advisor:

Dr. Kory Hedman (kory.hedman@asu.edu)

Including the members of the executive committee, the chapter has 50 members from the student body. The graduate power program at ASU has around 150-200 students.

For the spring 2016 semester, we have tentatively invited four speakers from the industry to give a one-hour long technical talk each. In addition, we plan to hold another social meet-and-greet towards the end of the semester. Further, as a social outreach to the community, members from the chapter would be volunteering for workshops organized by the Arizona Science Lab.

We can be reached on Facebook, LinkedIn or email:

Facebook: <https://www.facebook.com/ASUIEEESStudentChapter/>

LinkedIn: <https://www.linkedin.com/groups/8437949>

Email: ieepes.asu@gmail.com

Nikita Singhal
Deepak Ramasubramanian
Co-Chairs, IEEE PES ASU Student Chapter

Update from IEEE ASU Student Branch

IEEE held its 9th IEEEExtreme Competition on October 24th. This global programming competition spanned 24 hours featuring 2317 teams worldwide, with more than 220 from the United States. The ASU Student Branch participated in the competition for the first time, and brought two teams to compete. Digital Devilz was led by Chinedum Robert-Maduekwe with teammates Ian Mitchell and Gene Silva, and Zero Buggz was led by Allen Kawanzaruwa collaborating with teammates Zeyu Zhang and Christine Lam. Proctors were Carole Mars, Ronnie Litchfield, Joshua Colvin, and Phanindra Babu Guthi, as well as multiple members of ASU's IEEE student chapter, who provided moral support. Congratulations to team Zero Buggz that finished 26th in the United States, and in the top 200 in the world!



Ngoni Mugwisi
Chair, IEEE ASU Student Branch
ieeesuchair@gmail.com



Upcoming Conferences in Region 6

Hello IEEE Student Members!

[2016 IEEE 66th Electronic Components and Technology Conference \(ECTC\)](#) will be held on May 28-Jun 5, 2016

[2016 15th IEEE Intersociety Conference on Thermal and Thermomechanical Phenomena in Electronic Systems \(ITherm\)](#) will be held at Las Vegas, NV on May 31 – Jun 3, 2016

[2016 IEEE International Conference on Image Processing \(IEEE ICIP\)](#) will be held at the Phoenix Convention Center, Phoenix, AZ on September 25-28, 2016.

New Plans for a New Year

We had some good experiences this year and made some progress on several of our 2015 objectives in Region 6. My thanks to all the dedicated Region 6 volunteers who contributed to the IEEE in 2015. I wanted to wish you all a great holiday season and let you know what we are planning in 2016.

Here is a list of our objectives for 2016:

- Expand the chapter recording effort to other regions but have Region 6 lead the way—we will start with new training at the 2016 Region 6 OpCom meeting—more on this later.
- Increase membership in 2016—we will repeat our member reinstatement micro-volunteer project in 2016 but we also plan a targeted effort to get students to renew and help them transition to full members—we can actually reverse the long standing Region 6 membership decline in 2016.
- Streamline the operation of Region 6 conferences with the conference playbook being created by the GHTC advisory committee.
- Combine the Region 6 OpCom meeting January 29-31, 2016 in Las Vegas with Region 4 OpCom and a Region 1 and Region 5 Excom meeting—last year we only had Region 4 join us. This should be a great training and networking event. Note that the IEEE USA awards will be presented that Saturday night.
- Expand our PACE spending in 2016 to an even higher level in 2016 with requests for more money from IEEE USA and some Region money.
- Generate 4 IEEE Engineering Milestones outside of the Santa Clara Valley section.
- Create a link with Maker Media (creator of the Maker Faire events), First Robotics, Science Fiction Writers and other visionaries
- Create a coordinated R6 SIGHT committee and incorporate humanitarian projects in our new student training initiatives
- Create strong alliances between IEEE Region 6 and industry—create Region 6 Industrial Advisory Board
- In general: More PACE, more STEM, more industry, more FUN!

If you are interested in participating in any of these activities, please let us know. Best wishes to you in the New Year!

Tom Coughlin
Director, IEEE Region 6
tom@tomcoughlin.com



IEEE PHOENIX SECTION



Annual Banquet - Saturday, February 13th, 2016

Hilton Phoenix Airport, 2435 South 47th Street, Phoenix, Arizona 85034

Keynote Presentation

Title: Are there plenty more fish in the sea?

Speaker: Dr. David A. Demer

Senior Scientist and Leader, Advanced Survey Technologies Program
National Oceanic and Atmospheric Administration
8901 La Jolla Shores Drive
La Jolla, CA, 92037

Abstract

Most people are consuming more fish and are paying more for it. Less of it is wild caught and more is from aquaculture. Wild fish populations depend on ecosystem productivity, which is effected by a variety of forces such as El Niño, the Pacific Decadal Oscillation, climate change, ocean acidification, pollution, and fishing. These factors can change fish distributions, damage habitats, and alter biodiversity and ecological functions. In this context, we will explore the living resources in two of the most productive ecosystems on Earth: the Scotia Sea in the Southern Ocean, off the Antarctic Peninsula; and the California Current off the west coast of North America. We will learn how a variety of optical and acoustic instruments, and manned and autonomous vehicles are used to probe these remote and often inaccessible regions. In the process of counting and mapping exploited fish, we will touch on the complex interactions of climate, weather, seabed and oceanographic environments, avian and marine prey and predators, and fishers. We may enhance our appreciation for the effect the world ocean has on humanity, and vice-versa.

Speaker Biography



Dr. David A. Demer earned a B.S. in Electrical and Computer Engineering (ECE) from University of Arizona in 1986, worked as a Product Engineer for Intel Corporation from 1986 to 1989, received a Hertz Foundation Fellowship in 1989, and was awarded a Ph.D. in Applied Ocean Science / ECE from Scripps Institution of Oceanography (SIO), University of California at San Diego in 1994. He began his career with the National Oceanic and Atmospheric Administration at the Southwest Fisheries Science Center (SWFSC) as a Research Engineer with the Antarctic Ecosystems Research Division in 1990. He became the Leader of SWFSC's Advanced Survey Technologies Program (AST) in 1999, a Research Associate with the Marine Physical Laboratory at SIO in 2000, a Research Associate with the Integrative Oceanography Division at SIO in 2007, Guest Editor for the ICES Journal of Marine Science (JMS) in 2009, Review Editor for the JMS in 2012, and Senior Scientist at the SWFSC in 2010. Presently, he continues to lead the AST, acoustic-trawl surveys of fish and zooplankton, and the development and application of new marine and riverine sampling instruments and techniques. Over the last 25 years, Dr. Demer has designed and conducted investigations of zooplankton and fish stocks, predator-prey interactions, and ecosystems along the west coast of North America from the Sea of Cortes to the Bering Sea; along the east coast from the Gulf of Mexico to the Gulf of Maine; in the Irish, Ligurian, and Red Seas, off South Africa, and in the Southern Ocean.



IEEE PHOENIX SECTION



Annual Banquet - Saturday, February 13th, 2016

Hilton Phoenix Airport, 2435 South 47th Street, Phoenix, Arizona 85

Banquet Agenda

Registration / Social Hour:	5:30 PM – 6:30 PM
Sit-Down Dinner:	6:30 PM – 7:15 PM
Section Program:	7:15 PM – 8:00 PM
Keynote Presentation:	8:00 PM – 8:30 PM
Awards Presentation:	8:30 PM – 9:15 PM
Change of Section Officers:	9:15 PM – 9:30 PM

Business Attire is recommended

Banquet Web Links

Complete Banquet Information along with Banquet Registration: <http://sites.ieee.org/phoenix/>

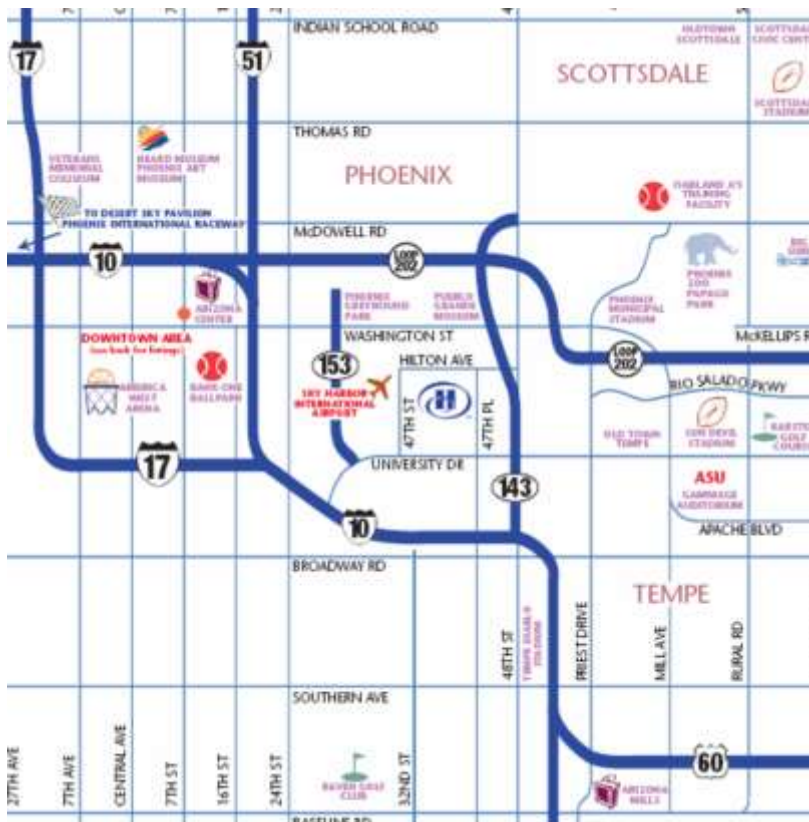
Banquet Registration Direct Link: <https://meetings.vtools.ieee.org/m/36447>

Banquet Venue



2435 South 47th Street
Phoenix, Arizona 85034
(480) 894-1600

West of Hohokam (143) Freeway and
North of University Drive



IEEE Phoenix Section Information

Please Support the IEEE Phoenix Section by Joining as Volunteer in Section Committees, Society Chapters, Affinity Groups, and Student Branches

IEEE Phoenix Section Executive Meeting on First Tuesday of the Month at Hilton Phoenix Airport

IEEE Phoenix Section Executive Committee Meeting is Open to Section Members

Visit the Section website at sites.ieee.org/phoenix/ for additional information

2016 IEEE International Conference on Image Processing (IEEE ICIP)

Phoenix Convention Center, Phoenix, AZ,

25-28 September 2016.

Website: 2016.ieeeicip.org

IEEE ICIP 2016 is the event for researchers, developers, product creators, educators and students who want to share, learn about, and advance the state-of-the-art in the areas of image/video processing, image/video communications, computer vision, computational imaging, and visual technologies based applications.

IEEE ICIP attendees include more than 1000 experienced researchers/developers including educators, engineers, computer scientists, and students, providing great networking and recruiting opportunities.

Important Dates:

Special Session and Tutorial Proposals: November 16, 2015

Paper Submissions: January 25, 2016

Visual Technology Innovation Award Nomination: March 31, 2016

Visual Technology Showcase Submission: May 15, 2016

IEEE ICIP 2016 highlights:

- nominate an individual or team for the Visual Innovation Award by 31 March 2016: This Award was created to recognize pioneers of transformative technologies and business models in areas within the technical scope of IEEE ICIP. The Award showcases innovations that have had great impact on human experiences with technology or are anticipated to do so in the near future. The Award Committee consists of well-known industry executives, visionary entrepreneurs, and scholars.
- maximize the visibility of your work via free open preview: Papers accepted to ICIP 2016 will (upon author approval) be available in their final accepted format on IEEE Xplore, freely accessible and downloadable by all in their final format from Aug 20, 2016 through September 30, 2016.
- maximize your networking and career connections: attendees will be given the opportunity to upload their CVs to be shared among interested recruiters for full-time, part-time, and consulting job opportunities. These CVs will be made available through a password-protected searchable platform to ICIP 2016 supporters/recruiters.
- experience state-of-the-art visual technology products and prototypes at the ICIP'16 Visual Technology Showcase. IEEE ICIP 2016 will feature a Visual Technology Showcase where technology creators and developers can present live demos of recent visual technologies and prototypes. *Participants who are interested in demoing their technology should submit a description of the technology at the IEEE ICIP 2016 website by 15 May 2016.*
- attend presentations, tutorials, and training courses by experts in the areas of image/video processing, image/video compression, computer vision, computational imaging, biomedical imaging, and other topics within the scope of IEEE ICIP 2016.



General Chair

Lina Karam
Arizona State University

Vice-General Chair

Aggelos Katsaggelos
Northwestern University

Technical Program Chairs

Fernando Pereira
Instituto Superior Técnico
University of Rochester

Innovation Program Chairs

Haohong Wang
TCL Research America

Jeff Bier
BDTI & Embedded Vision Alliance

Khaled El-Maleh
Qualcomm Technologies Inc.

Finance Chair

Sohail Dianat
Rochester Institute of Technology

Plenary Chairs

Michael Marcellin
University of Arizona
Sethuraman Panchanathan
Arizona State University

Special Sessions Chairs

Dinei Florencio
Microsoft Research
Chaker Larabi
Poitiers University
Zhou Wang
University of Waterloo

Challenge Sessions Chair

Dinei Florencio
Microsoft Research

Tutorials Chairs

Ghassan AlRegib
Georgia Tech
Rony Ferzli
Intel

Publicity Chair

Michel Sarkis
Qualcomm Technologies Inc.

Paper Awards Chairs

Vivek Goyal
Boston University
Ivana Tosic
Ricoh Innovations

Exhibits Chair

David Frakes
Arizona State University &
Google

Publication Chairs

Patrick Le Callet
Nantes University

Booxin Li

Arizona State University

Local Arrangement Chair

Pavan Turaga
Arizona State University

Registration Chair

Ricardo De Queiroz
Universidade de Brasilia

The 23rd IEEE International Conference on Image Processing (ICIP) will be held in the Phoenix Convention Centre, Phoenix, Arizona, USA, on September 25 - 28, 2016. ICIP is the world's largest and most comprehensive technical conference focused on image and video processing and computer vision. In addition to the Technical Program, ICIP 2016 will feature an Innovation Program focused on vision technologies and fostering innovation and networking. The conference will feature world-class speakers, tutorials, exhibits, and a vision technology showcase.

Topics in the ICIP 2016 Technical Program include but are not limited to:

Filtering, Transforms, Multi-Resolution Processing	Video Processing and Analytics
Restoration, Enhancement, Super-Resolution	Authentication and Biometrics
Computer Vision Algorithms and Technologies	Biological and Perceptual-based Processing
Compression, Transmission, Storage, Retrieval	Visual Quality Assessment
Computational Imaging	Scanning, Display, and Printing
Color and Multispectral Processing	Document and Synthetic Visual Processing
Multi-View and Stereoscopic Processing	Applications to various fields
Multi-Temporal and Spatio-Temporal Processing	

New initiatives at ICIP 2016 include:

- 1) Open preview for accepted papers on IEEE Xplore;
- 2) Visual Innovation Award (individual or team nominations due by 31 March 2016 at conference website);
- 3) Support for reproducible research;
- 4) Support for CV uploads on the ICIP site for full-time, part-time, and consulting job opportunities;
- 5) Visual Technology Showcase (submission due by 15 May 2016). For more details on these and other new initiatives at ICIP 2016, visit 2016.ieeeicip.org and connect now on the ICIP 2016 social media to get automatic updates about the various deadlines, sessions and events.

Paper Submission:

Prospective authors are invited to submit full-length papers at the conference website, with up to four pages for technical content including figures and references, and with one additional optional 5th page for references only. Submission Instructions, templates for the required paper format, and information on "no show" policy are available at 2016.ieeeicip.org.

Tutorials, Special Sessions, and Challenge Sessions Proposals:

Tutorials will be held on September 25, 2016. Tutorial proposals should be submitted at the conference website and must include title, outline, contact information, biography and selected publications for the presenter(s), and a description of the tutorial and material to be distributed to participants. For detailed submission guidelines, please refer to the tutorial proposals page. Special Sessions and Challenge Session Proposals should be submitted at conference website and must include a topical title, rationale, session outline, contact information, and a list of invited papers/participants. For detailed submission guidelines, please refer the ICIP 2016 website at 2016.ieeeicip.org.

Important Deadlines:

- Challenge Session Proposals: October 30, 2015
- Special Session and Tutorial Proposals: November 16, 2015
- Notification of Special Session and Tutorial Acceptance: December 18, 2015
- Paper Submissions: January 25, 2016
- Notification of Paper Acceptance: April 30, 2016
- Visual Innovation Award Nomination: March 31, 2016
- Visual Technology Showcase Submission: May 15, 2016
- Notification of Visual Technology Showcase Acceptance: May 30, 2016
- Revised Paper Upload Deadline: May 15, 2016
- Authors' Registration Deadline: May 15, 2016



World's FIRST Visual Innovation Award

Call for Nomination: The Award recognizes pioneers of transformative technologies and business models that have had great impact on human experiences or are anticipated to do so in the near future. The Award Committee consists of well-known industrial executives, visionary entrepreneurs, and scholars. Nominations are to be submitted online no later than 31 March 2016. The nominations will be forwarded to the Award Committee for selection of finalists who will be presented with their award at IEEE ICIP 2016. Please visit 2016.ieeeicip.org for more information and for the online submission form.

Nominate your favorite visual innovation TODAY! Details can be found at <http://2016.ieeeicip.org/VisualInnovationAward.asp>

Important Dates:

31 March 2016: Deadline for nominations

15 June 2016: Finalists announced

Award Committee



Giles Baker
SVP
Dolby Labs



Nikhil Bairam
CEO
Ricoh Innovations



Hanno Basse
CTO
20th Century Fox



Achin Bhowmik
VP
Intel



James Brallean
Managing Partner
Karmel Capital



Bill Dally
SVP
nVidia



Robert Gove
VP
Synaptics



Hsiao-Wuan Hsu
Chairman of ARD
Microsoft



Kevin Jou
CTO
MediaTek



C C Lee
SVP
Sony



Matthew Mengerink
VP
Google



Anthony Park
VP
Netflix



Raj Talluri
SVP
Qualcomm



Martin Vetterli
President
Swiss NSF NRC



Susie Wee
CTO
Cisco



Lina Karam
Professor
Arizona State



Aggelos Katsaggelos
Professor
Northwestern



Haohong Wang
General Manager
TCL



Khaled El-Maleh
Sr. Director
Qualcomm



Jeff Bier
President, Embedded
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IEEE

THE INSTITUTE OF ELECTRICAL AND ELECTRONICS ENGINEERS, INC.


 IEEE COMPONENTS, PACKAGING AND
MANUFACTURING TECHNOLOGY SOCIETY

IEEE Components, Packaging and Manufacturing Technology Society Phoenix Chapter

2015 Executive Committee for CPMT Chapter for IEEE-Phoenix Section

Position	Name	Phone Contact	Email Contact
Chair	Dr. Mahesh K. Shah	(480) 544-9438	mkshah@ieee.org
Asst. Chair	Mr. Vivek Gupta	(480) 734-2366	vmgupta@msn.com
Secretary	Dr. Rao Bonda	(480) 786-7749	r.bonda@ieee.org
Treasurer	Mr. David Dougherty	(480) 245-8099	david.dougherty@nxp.com
Program Chair	Dr. Vasudeva P. Atluri	(480) 227-8411	vpatluri@ieee.org
Asst. Program Chair	Bharat Penmecha	(480) 552 2511	bharat.penmecha@ieee.org
Tutorial & Workshop Chair	Dr. Vasudeva P. Atluri	(480) 227-8411	vpatluri@ieee.org
Website Co-Chair	Marc Liciardi		marc@dfxengineering.com
Website Co-Chair			

Tentative Schedule for Monthly Seminars

Date	Topic	Speaker
Jan. 20, 2016	Understanding and Managing the Key Cost Drivers in PCB Design to Optimize Performance and Cost	Marc Licciardi
Feb. 17, 2016	Roadmaps As We Approach The End of Moore's Law Scaling	Dr. Bill Bottoms
Mar. 16, 2016		
Apr. 20, 2016		
May. 18, 2016		
Jun. 15, 2016		
Jul. 20, 2016		
Aug. 17, 2016		
Sep. 21, 2016		
Oct. 19, 2016		
Nov. 16, 2016		
Dec. 14, 2016		



THE INSTITUTE OF ELECTRICAL AND ELECTRONICS ENGINEERS, INC.



IEEE COMPONENTS, PACKAGING AND MANUFACTURING TECHNOLOGY SOCIETY

IEEE Components, Packaging and Manufacturing Technology Society Phoenix Chapter

Wednesday, January 20th, 2016 at 5:30 PM

Understanding and Managing the Key Cost Drivers in PCB Design to Optimize Performance and Cost

Marc Licciardi

CEO and Lead Engineer

DfX Engineering
Scottsdale, AZ 85254 USA
marc@dfxengineering.com

ABSTRACT

Printed circuit boards have become increasingly complex and diverse in their applications and design. Processing sequences can be over fifty steps long, leading to a number of design choices. While the process is complex, the fundamental cost drivers can be broken into a few significant categories. These categories can be translated into specific design goals, which can then inform the best cost-performance optimized design. This presentation will step you through the optimization process and highlight the cost and performance choices.

BIOGRAPHY



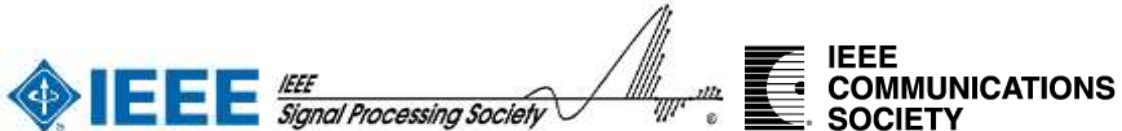
Marc Licciardi is the founder of DfX Engineering. His work focuses on supporting large OEMs to develop cost- and performance-optimized PCB designs for applications ranging from very high-volume consumer devices to high-reliability data center networking devices. Prior to founding DfX Engineering, he was an SVP at Gold Circuit Electronics in Taiwan. He also held design engineering roles at Adflex Solutions, Hughes Aircraft and Parlex Corporation. He has worked on flex, rigid flex,

wafer probes, substrates, standard and HDI PCB's. Licciardi holds a BSIE degree from Worcester Polytechnic Institute.

-
- Date:** Wednesday, January 20st, 2016
Location: Group Conference Room, Freescale Semiconductor, Inc., Discovery Business Center, 2100 E. Elliot Rd. Tempe, AZ. Enter the facility through the Main (South) Lobby in building 94 and sign in with Security (*Photo ID required*) **BEFORE 6:00 PM**. You will be escorted to the meeting room. The presentation promptly starts at **6:00 PM**.
Agenda: 5:30–6:00 PM: Social/Refreshments, 6:00–7:00 PM: Presentation, 7:00 PM: Dinner (Pizza and Soda will be provided by the IEEE Phoenix Section CPMT Society Chapter)
IEEE members and non-members are all welcome to attend. **Please arrive at the facility entrance no later than 5:45 PM.**
-

For more information, please contact any of the following CPMT officers:

Vasu Atluri	(480) 227-8411	Rao Bonda	(480) 786-7749	David Dougherty	(480) 413-6923
Vivek Gupta	(480) 734-0266	Bharat Penmecha	(480) 552-2511	Mahesh Shah	(480) 544-9438
Marc Licciardi					



SP-COM Phoenix Chapter

Please join our Google Group!

Please join our increasingly popular Google group to get the most up-to-date information about the society's activities. We have now over 50 members who are availing of this facility. Email traffic is thin, and used only to send meeting notices. No spam !

<https://groups.google.com/d/forum/ieee-sp-com-phoenix-chapter>

**In addition, we continue to post meeting notices on IEEE vtools at
(<https://meetings.vtools.ieee.org/main>)**



Phoenix Chapter of the IEEE Computer Society January, 2016

News

Our final chapter meeting of 2015 was held on Wednesday, December 2nd, at the Phoenix (Central) Campus of the ITT Technical Institute. The campus is located at 10220 N 25th Avenue, Phoenix. The speaker was Mark Goldstein of International Research Corporation. He gave an excellent presentation on the Internet of Things.

Chapter elections for the 2016 officers were held at this meeting. Two things of note:

- The chapter decided, to be consistent with the Executive Committee, to move to two years terms for officers.
- The current slate of officers was re-elected to serve in 2016.

Please note: beginning in 2016 we will hold our meetings on the **second** Wednesday of the even numbered months. Thus, our first meeting in 2016 will be on Wednesday, February 10th. The program for this meeting, which we hope to convene on the east side, is being developed.

Visit the CS Chapter website for the latest information: <http://ewh.ieee.org/r6/phoenix/compsociety/>.

For brief announcements regarding upcoming events we are also on Twitter: @IEEECS_PHX

If you would like to suggest a topic or speaker for any of our future meetings, please contact one of the chapter officers:

Chair	Jerry Crow	jerry.crow@computer.org
Vice-chair	Dr. Brad Morantz	bradscientist@ieee.org
Secretary	Audrey Skidmore	askidmore@computer.org
Treasurer	Diane Smith	sdianesmith@computer.org
Webmaster	Audrey Skidmore	askidmore@computer.org



Technical Presentation and Administrative Meeting

Meeting February 16, 2015

Program Presentation: Pathways to Next-Generation Photovoltaics

This talk will set the stage for understanding and appreciating the latest advances and central challenges in photovoltaics research. Over the long term, nanotechnology is expected to enable improvements throughout the energy sector, but the most striking near- to mid-term opportunities may be in lower-cost, higher-efficiency conversion of sunlight to electric power.

There are multiple ways to improve photovoltaic performance by means of nanostructures in solar cells: (1) employing new physical approaches in order to reach thermodynamic limits; (2) allowing solar cells to more closely approximate their material-dependent thermodynamic limits; and (3) providing new routes for low-cost fabrication by self-assembly or design of new materials. We focus primarily on the first two avenues, both of which have the goal of increasing efficiency.

Several different approaches will be described that circumvent long-held physical assumptions and lead beyond first- and second-generation solar cell technologies. Special emphasis will be on novel nanostructure-based devices based on advanced concepts such as hot carrier cells, intermediate band and multi-exciton generation, which offer the theoretical basis to realize high-efficiency energy conversion. In particular, we focus on the role of ultrafast carrier dynamics in nanostructures in terms of the competition between carrier extraction processes and energy relaxation processes that convert electron kinetic energy into heat. We also focus on the effects that surfaces and interfaces play in nanostructured solar cells, and on how to reduce parasitic carrier recombination effects through passivation.

Speaker: Stephen M. Goodnick received his Ph.D. degrees in electrical engineering from Colorado State University, Fort Collins, in 1983, respectively. He was an Alexander von Humboldt Fellow with the Technical University of Munich, Munich, Germany, and the University of Modena, Modena, Italy, in 1985 and 1986, respectively. He served as Chair and Professor of Electrical Engineering with Arizona State University, Tempe, from 1996 to 2005. He served as Associate Vice President for Research for Arizona State University from 2006-2008, and presently serves as Deputy Director of ASU Lightworks, and is Hans Fischer Senior Fellow with the Institute for Advanced Studies at the Technical University of Munich. Professionally, he is currently serving as Past-President (2013-2014) of the IEEE Nanotechnology Council, and served as President of IEEE Eta Kappa Nu Electrical and Computer Engineering Honor Society Board of Governors, 2011-2012. Some of his main research contributions include analysis of surface roughness at the Si/SiO₂ interface, Monte Carlo simulation of ultrafast carrier relaxation in quantum confined systems, global modeling of high frequency and energy conversion devices, full-band simulation of semiconductor devices, transport in nanostructures, and fabrication and characterization of nanoscale semiconductor devices. He has published over 350 journal articles, books, book chapters, and conference proceeding, and is a Fellow of IEEE (2004) for contributions to carrier transport fundamentals and semiconductor devices.

NOTE MEETING STARTS AT 11 AM

Meeting Agenda:

11AM: Attendee introductions
11.05 Lunch

11:20 AM: Program Presentation
12:20 Officer's report
12:45 Admin. Meeting / Officers

Where: SRP's **PERA Club Bighorn Room**,
1 East Continental Drive, Tempe, AZ
Continental is West of 68th St., ½ mile south of McDowell Road

Enter the Private PERA Club and follow drive to large parking lot. Big Horn is small building at South East corner of lot.

When: Tuesday, **February 16th - 11:00am – 1:00pm**, Registration fee is \$15. This fee will include lunch provided by the PERA Club.

Lunch: TBD

RSVP: Please advise Ronald Sprague r.sprague@ieee.org if you plan to attend so accounting for lunch is possible.

Technical Presentations: The Program Chairs are seeking suggestion from members for future presentations. Any ideas of interest are open for consideration. Please contact Barry Perlman Program Chair at barry.perlman@gmail.com.

About IEEE Phoenix Section Life Member Affinity Group: The IEEE Phoenix Section Life Member Affinity Group was organized to enable IEEE Life Members to retain active IEEE associations, contribute to the social good in their communities, advance IEEE's professional interests and enjoy each other's company.

Activities: Technical meetings scheduled the 3rd Tuesday of February, May, October, and December. Elections are held at the December meeting.

Future Technical Meetings: All meeting are scheduled at the SRP PERA CLUB. It is suggested you put these dates on your calendar to attend the meetings.

- Tuesday February 16, 2016
- Tuesday May 16, 2016****
- Tuesday October 18, 2016
- Tuesday December 20,2016

The Officers for 2016 are as indicated below.

Officers:

Chair	Leslie Daviet II	lesdavietii@cs.com
Vice Chair	Jim Tang	JFTANG@cox.net
Secretary	Tom Lundquist	tom.lundquist@ieee.org
Treasurer	Gary Frere	gary.frere@gmail.com
Membership	Rao Thallam	Rao.Thallam@gmail.com
Facilities	Ron Sprague	r.sprague@ieee.org
Program	Barry Perlman	barry.perlman@gmail.com
Past Chair	Barry Cummings	abarrycummings@gmail.com

**** Note: Date change this is a Monday



IEEE Phoenix Section Officer Terms

At the November 3, 2015 Executive Committee (ExCom) meeting of the IEEE Phoenix Section officers and representatives of the affiliated Chapters and Affinity Groups, a decision was made to change the term of IEEE Phoenix Section Officers from one year to two years in accordance with the bylaws the Phoenix Section operates under. The rationale of this decision included operational efficiency and effectiveness of individuals being in a position for two years and thus more able to implement improvements; program continuity and planning; and improved section operations.

Per the Phoenix Section website (<http://sites.ieee.org/phoenix/about/section-bylaws/>), the Phoenix Section is “required to operate in accordance with IEEE Constitution, Bylaws, Policies, and the MGA Operations Manual.” Per Section 9.4.F.7.b. of the MGA Operations Manual:

- “The term of office for all officers shall be one or two years. A Section must define the officer term as one or two years and record it in its local operating procedures document; if the officer term is not recorded in the local operating procedures document, it shall be two years. An individual may continue in the position until a successor has been duly elected and takes office.”

There may be times when an individual is unable to serve for two years in a position so annually the Nominating Committee will confirm with each officer their willingness and ability to serve in the specific position for the second year of their term. If someone needs to drop out after their first year or be replaced, the Nominating Committee will identify replacements and place them on the ballot for election. Typically, the officer succession plan is to request individuals serving as officers to progress through the various positions but this is not a requirement.

The purpose of this notice is to record the decision reached regarding Phoenix Section Officers serving two year terms. Please contact any of the Phoenix Section Officers if you have questions or input. Thank you.

Executive Committee Meeting

No meeting of Executive Committee in July & August

Normal meetings are on first Tuesday of the month from 6:00 PM to 8:00 PM
The Airport Hilton Phoenix,
2435 S 47th St. Phoenix, AZ 85034, (480) 894-1600.

2016 Executive Committee

Chair: Bruce Ladewig
Vice Chair: Surinder Tuli
Secretary: Vivek Gupta
Treasurer: Mahesh Shah
Past Chair: Barbara McMinn

Executive Committee Meetings

Date: First Tuesday of every month, except July and August
Time: 6:00 – 8:00 p.m.
Location: Hilton Phoenix Airport, 2435 South 47th Street, Phoenix, AZ 85034

IEEE Phoenix Section: Calendar of Activities

For any questions and inputs regarding the calendar of activities, please contact Dr. Surinder Tuli, Vice Section Chair, at Surinder.tuli@gmail.com.

January 2016

Annual Banquet: Awards selection and notification
Officer's Appreciation Dinner
Finalize Section committee Chairs and Chapter chairs and updated website
Scholarship Applications due
Scholarship Awards announced
Concentration Banking Account year end statements available
Future City competition:
Regional final: TBD
Judges to review: TBD
Coordinator: TBD
Deadline for submission of 1099 reports and bank account disclosure form

The Rising Stars conference

The Rising Stars conference brings together the most promising students and young professionals from within the region to network and be inspired by each other and the top tech companies from around the world. It will be taking place the first week in January, 2016 at the Excalibur in Las Vegas, Nevada.

<http://sites.ieee.org/risingstars/>

February 2016

Annual Banquet: February 12

Budget planning: Chapters and Affinity Groups

Deadline for submission of L50 reports to receive the 10% bonus

IEEE Board of Directors meeting

Section participation in E-Week Feb 22-28 at Arizona Science Center (Engineer's Day 2-28)

February 2016: *Wearable Robotics Association Conference (wearRAcon) occurring within your section. The conference is scheduled to be held in Phoenix, US on 2016-02-10 to 2016-02-12. (IEEE Robotics and Automation Society participating as a sponsor of this event.)*


IEEE Senior Member and Fellow Grade

IEEE Phoenix Section Membership Development would like to nominate eligible IEEE Members from the Section to Senior Member and Fellow Grades. Please review the requirements at www.ieee.org for eligibility.

Eligible candidates are requested to send in their resumes to Dr. Vasudeva P. Atluri, Membership Development Coordinator, at vpatluri@ieee.org and Dr. Bruce Ladewig, Section Chair, at bruceladewig@ieee.org for consideration.

Phoenix Section LinkedIn Group


If you are interested in professional networking and shared Section related updates & discussions join the [IEEE Phoenix Section Group on LinkedIn](#). Signing up only takes minutes and is free. A job board is available as well.

You can also go to IEEE Phoenix Section LinkedIn page by clicking  button on the [IEEE Phoenix Section home page](#)

IEEE Phoenix Section Ventures into Social Media

You can access the web page three ways:

Use the URL: <https://www.facebook.com/IEEEPhoenixSection>

Click on the Facebook logo  link from [IEEE Phoenix section home page](#).

Search for IEEE Phoenix Section from your Facebook page.

We need following help.

1. Each of you access the IEEE Phoenix Section Web page and click on "Like" hyperlink.
2. Go on the Friends section of the page and "Invite Your Friends." Once you click on Invite button, it will get your email contact list. Your facebook contact list will already be populated with your Facebook friends and you can simply click the Invite button next to their name. Please invite as many friends as you can.
3. Provide me the contents for posting on a regular basis - meeting/ event announcements, Event pictures, Videos.
4. Start some discussion topics under - Status section.

IEEE Membership Grade Advancement

IEEE Phoenix Section Executive Committee encourages all to apply for advancement in membership grade to Senior Member and Fellow Grade. Please review the requirements at www.ieee.org. Please contact IEEE Phoenix Section Membership Development Chair, Dr. Vasudeva P. Atluri, at vpatluri@ieee.org for additional information.

Enhanced Senior Member Application Launched

Effective 29 July 2011, IEEE Admission and Advancement launched a [new Senior Member Application](#). The new application includes numerous enhancements, based on feedback from volunteers and members, including:

- New user friendly format / design
- Secure environment (need IEEE Web account)
- Ability to save application in “draft” form
- Ability to upload resume or Curriculum Vitae (up to 3 MB)
- Applicant can view application online
- Applicant can view status of requested reference forms
- References will be notified by email to provide applicant reference
- References will have the ability to view their completed reference form(s)
- Real time application status

The goal is to provide prospective Senior Members with an easy to use and intuitive interface, while streamlining internal operations at the same time. [View the new Senior Member application](#).

IEEE Member's Benefits



Connect with other technology professionals

Collaborating with a community that shares similar interests can help you take your career to the next level. That's why we created IEEE Collabratec, an online community where technology professionals are networking, collaborating, and creating globally. Here's some of what you can do on IEEE Collabratec:

- Participate in discussions with other professionals.
- Access interactive communities like these:



- Cloud Computing, Big Data, Transportation Electrification, Internet of Things, All-Society Tech Forum
 - Technology networks in Africa, Asia/Pacific, Europe, Middle East, North America, and South America
 - Women in Leadership, IEEE Young Professionals, IEEE Author Lab, IEEEExtreme, IEEE Day, CareersConnect-USA
- Create Private Groups for research projects or to organize activities.

Free IEEE-USA eBook Offered as Special Benefit to IEEE Members

IEEE-USA's Free E-Book to Members in December Examines "Building a Culture that Develops Leaders and Managers"

WASHINGTON (1 December 2015) -- Beginning today, IEEE members can download a free copy of the IEEE-USA E-Book "Leading and Managing Engineering & Technology -- Book 3: Building a Culture that Develops Leaders and Managers."

Author Gus Gaynor shares that developing an organization that fosters leadership is both simple and complex, depending on what's involved.

Among the topics are "The Basic Organizational Culture," "Building a Leadership Culture," and a "Case Study: IBM's Silverlake Project."

The case study provides an example of how an entire culture can be transformed, and what happens when a project manager takes personal initiative, asks questions, listens to the responses and builds trust among team members. Gaynor cautions that making such a transformation happen takes hard work and cooperation from many people.

From 1 December to 15 January, you can get your free e-book by going to <http://shop.ieeeusa.org/usashop/product/careers/74735>. Log in with your IEEE Web account, add the book to your cart and use promo code DECFREE at checkout.

In January, IEEE-USA E-Books will offer Book 4 in the series, "What it Takes to be A Manager-Leader." Gaynor offers that, "It is impossible to manage without leading" and "lead without managing."

CALL FOR AUTHORS

IEEE-USA E-BOOKS seek authors to write an individual e-book, or an e-book series, on career guidance and development topics. If you have an idea you think will benefit members in a particular area of expertise, please email your proposal to IEEE-USA Publishing Manager Georgia C. Stelluto at g.stelluto@ieee.org and IEEE-USA E-Book Chair Gus Gaynor at g.gaynor@ieee.org.

IEEE-USA serves the public good and promotes the careers and public policy interests of nearly 200,000 engineering, computing and technology professionals who are U.S. members of IEEE.

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Contact: Sharon C. Richardson, Coordinator
IEEE-USA Communications & Publishing
Phone: [1 202 530 8363](tel:12025308363)
E-mail: s.richardson@ieee.org

Member Discounts

GetInsured

Did you know that as an IEEE member, you now have access to Mercer Marketplace* powered by GetInsured? Mercer Marketplace is the easiest way to shop for health insurance. It also offers a wealth of entertaining, educational content to help you understand your options.

As an IEEE member, you have the freedom to compare available plans and secure a solution that strikes the best balance between coverage and affordability. Plus, you can do this **confidentially and conveniently in minutes**. You pay no additional cost to take advantage of this service.

2016 Open Enrollment for health insurance starts soon! [Learn more about the Mercer Marketplace powered by GetInsured.](#)

* Provided by Mercer Health & Benefits LLC

These individual health insurance plans are not sponsored by IEEE nor the Mercer Marketplace. Available in the United States.

Lenovo

IEEE members can get a discount from Lenovo*, a global leader in the PC marketplace. Members save up to 30% off the everyday public web price of Lenovo's entire product line of laptops, tablets, desktops, servers, accessories and more! Take advantage of great deals on top products for the home and office, including the award-winning ThinkPad laptops and innovative multimode YOGA tablets.

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Daydreaming about goals can be exciting. It can also be daunting when we aren't sure how to achieve them. This is where financial wellness comes in. Most financial worries come from lack of planning, not lack of money.

That is why we are pleased to let you know that you have access to My Financial Wellness for IEEE Members, a new program available in the US.

Whether you are well along your financial plan or had some bumps along the way, you still can benefit from financial planning. At My Financial Wellness for IEEE Members, you can benefit from no-cost educational resources, financial tools, and credentialed financial advisors to help you get your financial plans in shape.

To learn more, visit IEEEFinancialWellness.com.



IEEE-USA Webinars

Date Revisions for some IEEE-USA Webinars

IEEE-USA has had to reschedule the webinar [From Page to Stage](#) originally planned to take place in October. This webinar will now take place on 19 November at 2:00pm EST. IEEE-USA has partnered with the [IEEE Professional Communication Society](#) to address a topic that has confounded everyone from students to professionals at one time or another. *From Page to Stage* will discuss how to take the written version of your work from its form in a lab notebook, a report, or a paper and translate it into a presentation or poster. Dr. Julia M. Williams is Executive Director of the Office of Institutional Research, Planning, and Assessment and Professor of English at Rose-Hulman Institute of Technology and the presenter for this webinar. She will offer a clear process for making this transition successfully and she will provide concrete strategies for making the most effective use of presentations. [If you haven't registered for this webinar you still have time!](#)

Leadership

IEEE-USA has revised its upcoming webinars on leadership. Elizabeth Lions, who earlier this year provided three great webinar presentations on the topic of leadership, will return this fall to IEEE-USA with one more webinar presentation on the topic of leadership and a webinar on emotional intelligence. [Situational Leadership](#) is the key to driving teams from being adequate to spectacular. Situations will arise in the office, but as a leader it's your responsibility to get the team headed in the right direction. [Emotional Intelligence](#) is the ability to monitor one's own and other people's emotions to discriminate between different emotions and label them appropriately, and to use emotional information to guide thinking and behavior. Elizabeth will explain what emotional intelligence is, why it's important and how to develop the skill. Situational Leadership and Emotional Intelligence will take place on Friday 13 November and 11 December from 1:00pm 2:00pm EST. Again, you can register for both of these webinars

today at the [IEEE-USA website](#).



[Introducing the Kalman Filter](#)

This tutorial is a guide to how the Kalman Filter works. Dr. Ramsey Faragher explains that even students without a strong mathematical background can understand what the Kalman Filter can do to smooth measurements and fuse data together.



[AuthorLab: Information on the IEEE article processing charges](#)

This video describes the payment options offered by IEEE to pay Article Processing Charges.



[SIMD Programming in VOLK, the Vector-Optimized Library of Kernels](#)

To improve the speed of signal processing and computation, Tom Rondeau, Nick McCarthy, and Tim O'Shea walk through the VOLK (Vector Optimized Library of Kernels) library for SIMD (Single Instruction Multiple Data) Programming.

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Refer a Colleague, Get Great Merchandise!

In conjunction with IEEE-USA, IEEE is excited to continue this special offer for US members. Refer a colleague to IEEE. If they join before the end of the membership year, they'll get a 50% discount off their first year membership, and you'll get a gift. IEEE-USA gift items you can choose from include:

- Cooler backpack
- Portable solar battery charger
- Portable speaker
- Parker ballpoint pen
- Golf balls
- Golf umbrella

- Computer backpack
- Tablet case
- Parker pen and mechanical pencil set
- Travel coffee mug and tumbler set
- Baseball hat
- Travel umbrella

Members remain eligible to receive incentives through the existing IEEE Member-Get-a-Member (MGM) program. How it works:

- Refer your colleague via the [online form](#).
- Your referral will receive an email inviting them to join at a 50% discount off first-year membership dues, and will receive benefits through 31 December 2017.
- Your referral will provide your IEEE member number when he or she joins.
- Within 1-2 weeks after your referral joins, you will receive an email confirming your recruiting success, including a link which allows you to select your IEEE-USA merchandise item.
- You will receive a separate email for each new member you recruit.

Please help spread the word and share your IEEE experience - no one knows how beneficial IEEE Membership is to technical and career development better than you, the member. [Refer your friends and colleagues today!](#)

IEEE Mentoring Program

IEEE MentorCentre is the online mentoring platform for IEEE. This is a valuable resource for IEEE members seeking a professional mentoring partnership. That means all the best practices you have come to expect from an IEEE mentoring program are in place, with ample opportunity to enter a unique mentoring partnership not found anywhere else.



What you will find with IEEE MentorCentre:

- The ability to connect with mentors based on specialized areas of practice, experience, IEEE societal affiliation and more
- Opportunities to give back to the profession by registering as a mentor
- Additional fields to narrow down the preferred profile of the mentor
- Improved mentor controls allowing you to control how you are viewed in the system

Participation in the program is voluntary and open to all IEEE members above the grade of Student member.

[Access IEEE MentorCentre](#)

IEEE ResumeLab

IEEE members have a powerful tool to help gain a competitive edge in the employment process. IEEE ResumeLab is an online service that allows IEEE members to develop a resume *or* curriculum



vitae using specialized tools tailored for each step of the job seeking process. This product is added to the list of offerings that assist members as they find jobs and develop their careers.

IEEE ResumeLab is designed with a series of modules that assist the member through the employment process. Key modules and features include:

- **Resumes** - Select from a wide array of templates geared toward specific industries, sectors and work experience stages.
- **Letters** - From cover letter to post-interview thank you letter, ensure optimal communication throughout the hiring process.
- **Skills Assessment** - Highlight the skills you possess, your competency in those skills, and what makes your experience with these skills unique.
- **Mock Interviews** - Prepare for the real thing by selecting an interviewer and the type of questions they'll ask. Choose to record your interview for evaluation and feedback.
- **Video Resumes** - Record custom video messages for potential employers.
- **Portfolios** - Upload and organize your past work to present to potential employers.
- **Share Online** Publish and share everything you create on a publicly viewable website.

[Access IEEE ResumeLab](#)

Call for Nominations: IEEE Technical Field Awards

Nominations are due 31 January annually for the IEEE Technical Field Awards (TFA). IEEE TFAs are awarded for contributions or leadership in a specific field of interest of IEEE and are among the highest awards presented on behalf of the IEEE Board of Directors.

All IEEE members are encouraged to submit a nomination for a worthy candidate within their technical fields. Nomination forms and award-specific criteria can be [downloaded](#).

Since 1917, the IEEE Awards Program has paid tribute to technical professionals whose exceptional achievements and outstanding contributions have made a lasting impact on technology, society, the engineering profession, and humanity. By this means, the image and prestige of the organization, its members, and the profession are all enhanced.

For more information visit the [Awards program](#) online or email awards@ieee.org.



Access GoogleApps@IEEE via myIEEE

Did You Know?

IEEE members can quickly access their GoogleApps@IEEE service via myIEEE. The GoogleApps@IEEE gadget is visible to all members on the "myDesktop" page in myIEEE. Members can remove, relocate, or add the gadget back at any time. Users will experience:

- Services and account settings hot-linked to respective locations in GoogleApps (separate window)
- Single sign-on (no need to reauthenticate)

After signing in to myIEEE, select the Customize tab and begin personalizing your myDesktop page.

